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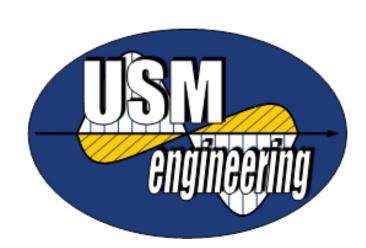
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Analog Filter Design System using Mathematica and System Modeler

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Analog filters are an essential part of modern electronics; however, their design, realization and verification can be arduous and time consuming. This project describes a Mathematica and SystemModeler platform for automated, fast analog filter design and simulation. The platform consists of two key components:

- SystemModeler library of first and second order low-pass, high-pass, band-pass and band-stop filters based on some of the most popular filter topologies: Sallen-Key, Tow-Thomas, State Variable, Multiple Feedback, Bainter and Boctor circuits and
- II. Mathematica package based on Butterworth, Chebyshev and elliptic frequency response approximations and different filter topologies.

For a given set of design specifications, the program returns overall transfer function, number of first and second order filter stages to be cascaded, and transfer function, gain, cutoff frequency and quality factor for each stage. For any userselected filter topology, the program builds the circuit model, returns values of resistors and capacitors for each stage, applies them to the model and simulates the circuit to verify the time and frequency responses of the model. We therefore, have a fast and effective means of filter realization and verification, with programmatic control of simulation and filter components.

The library named "Filters" is a custom made SystemModeler library of first and second order active low-pass, high-pass, band-pass and band-stop filters based on some of the most popular filter topologies: Sallen-Key, Tow-Thomas, State Variable, Multiple Feedback, Bainter and Boctor circuits (Figure 1 and Figure 2.)

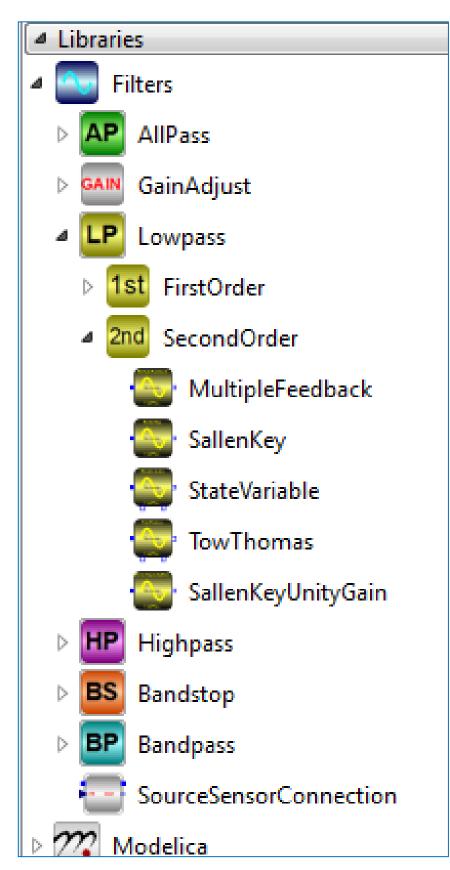


Figure 1. Screenshot of SystemModeler custom made library "Filters"

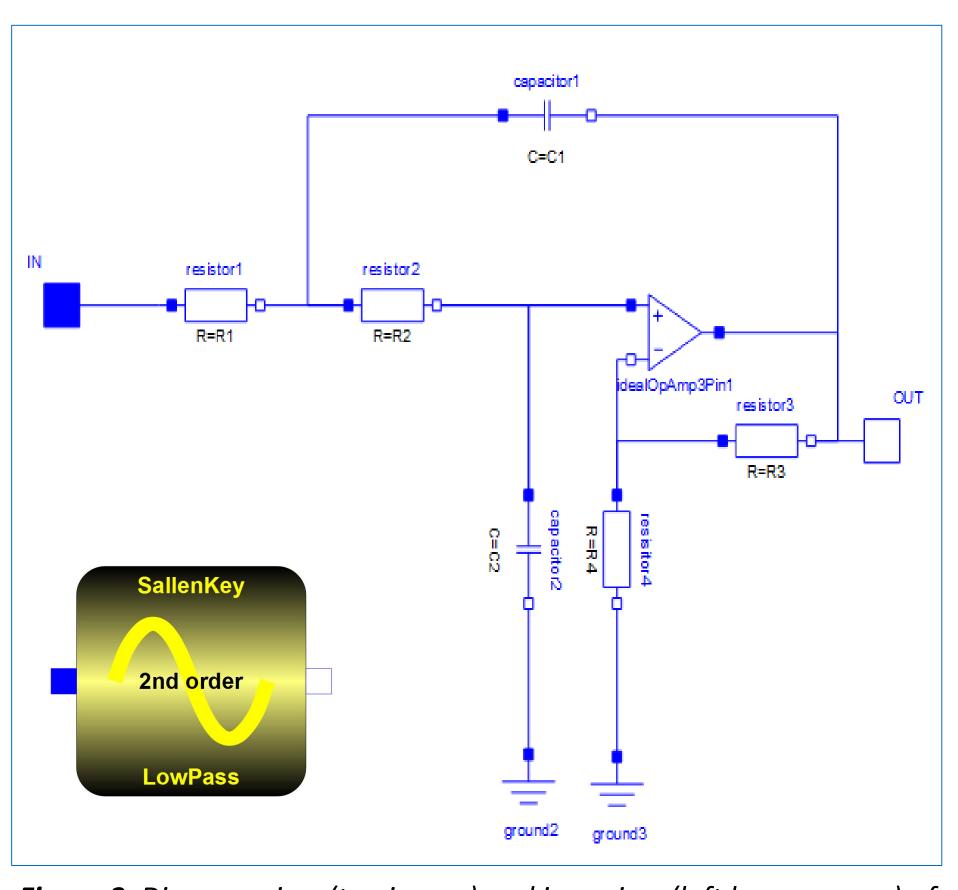


Figure 2. Diagram view (top image) and icon view (left lower corner) of low-pass second order Sallen-Key circuit.

AnalogFilterDesign application is a Mathematica based package written in Wolfram Language that consists of three sets of function dedicated to solving three fundamental problems of filter design, namely approximation problem, circuit analysis and circuit synthesis.(Figure 3).

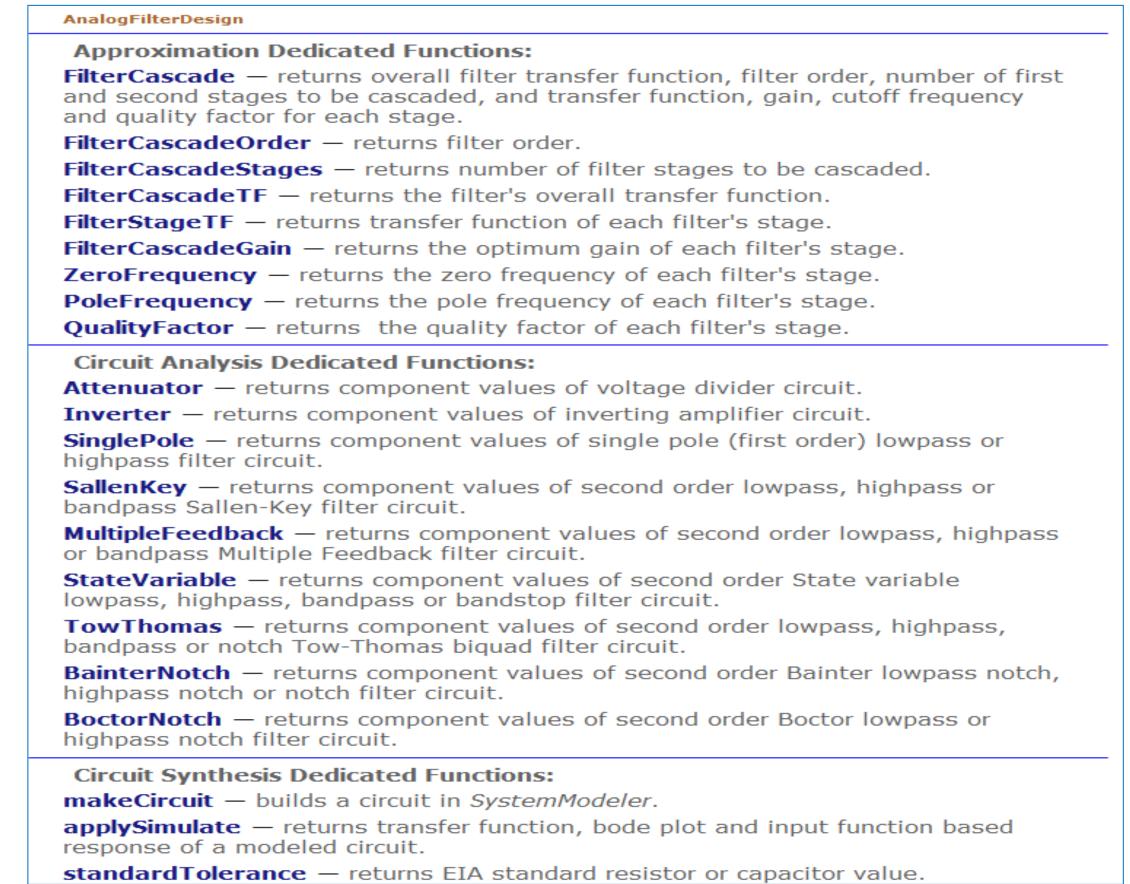


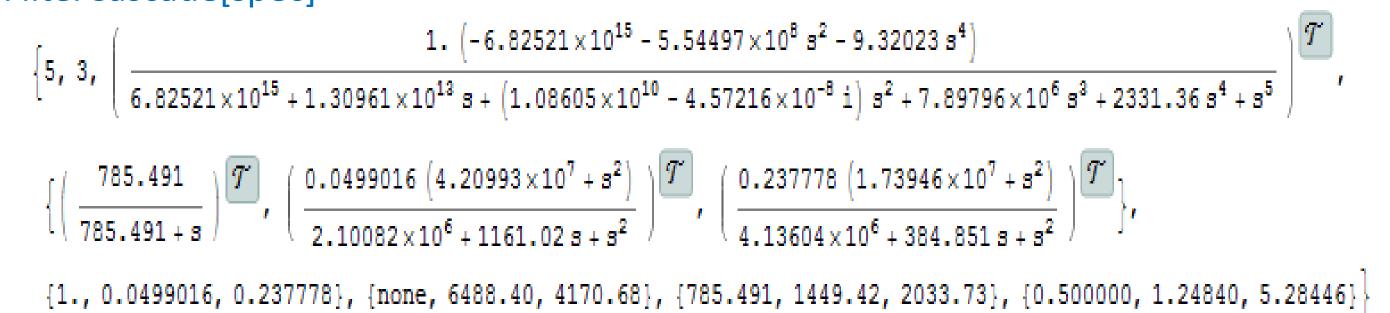
Figure 3. List of functions included in AnalogSystemDesign package

Design specification: $a_p = 0.5 dB$, $a_s = 66.dB$, $\omega p = 2000 rad/s$, $\omega s = 4000 rad/s$, gain = 0

spec={{"Lowpass",{2000.,4000.},{0.5,66.}},EllipticFilterModel,0};

Filter order, Number of Stages, Transfer Functions, Gain Distribution, Zero and Pole Frequency, Quality Factor:

FilterCascade[spec]



Based on program output above, Bode plot is obtained for each stage and the overall transfer function (Figure 4.)

Filter Components:

sp=SinglePole[{"Lowpass",FilterCascadeGain[spec,1],PoleFrequency[spec,1],3.3*10⁻⁷}] {{SinglePole1.Rin->3857.85,SinglePole1.Rf->3857.85,SinglePole1.C1->3.3*10⁻⁷}}

sv=StateVariable[{"Bandstop",FilterCascadeGain[spec][[2;;3]],ZeroFrequency[spec][[2;;3]],P oleFrequency[spec][[2;;3]],QualityFactor[spec][[2;;3]],3.3*10⁻⁷,1000.}]

{{StateVariable1.R1->1000.,StateVariable1.R2->1000.,StateVariable1.R3->1000.,StateVariable1.R4->2090.7,StateVariable1.R5->2090.7,StateVariable1.R6->2745.21,StateVariable1.R7->1000.,StateVariable1.R8->20039.4,StateVariable1.R9->1000.,StateVariable1.R10->1000.,StateVariable1.C1->3.3*10⁻⁷,StateVariable1.C2->3.3*10⁻⁷},{StateVariable2.R1->1000.,StateVariable2.R2->1000.,StateVariable2.R3->1000.,StateVariable2.R4->1490.02,StateVariable2.R5->1490.02, State Variable 2.R6->14853.4, State Variable 2.R7->1000., State Variable 2.R8->4205.61, State Variable 2.R9->1000.,StateVariable2.R10->1000.,StateVariable2.C1->3.3*10⁻⁷,StateVariable2.C2->3.3*10⁻⁷}}

After the component values are obtained,, circuit is synthesized, simulated and verified using functions makeCircuit and applySimulate functions- circuit is simulated for step and chirp input functions. (see Figure 5, 6, and 7).

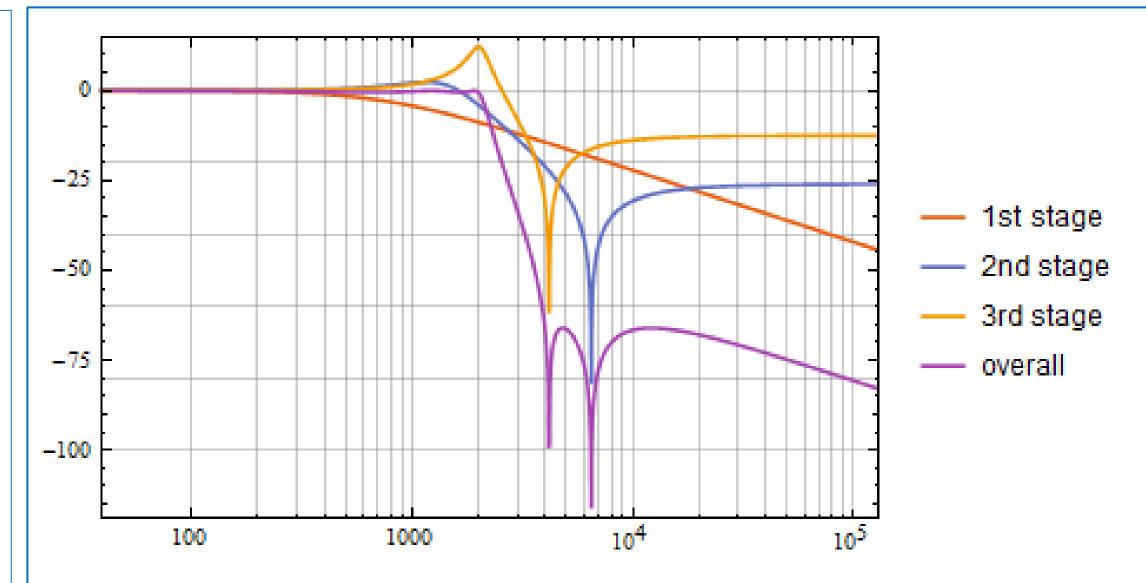


Figure 4. Bode Plot of the filter and each filter stage.

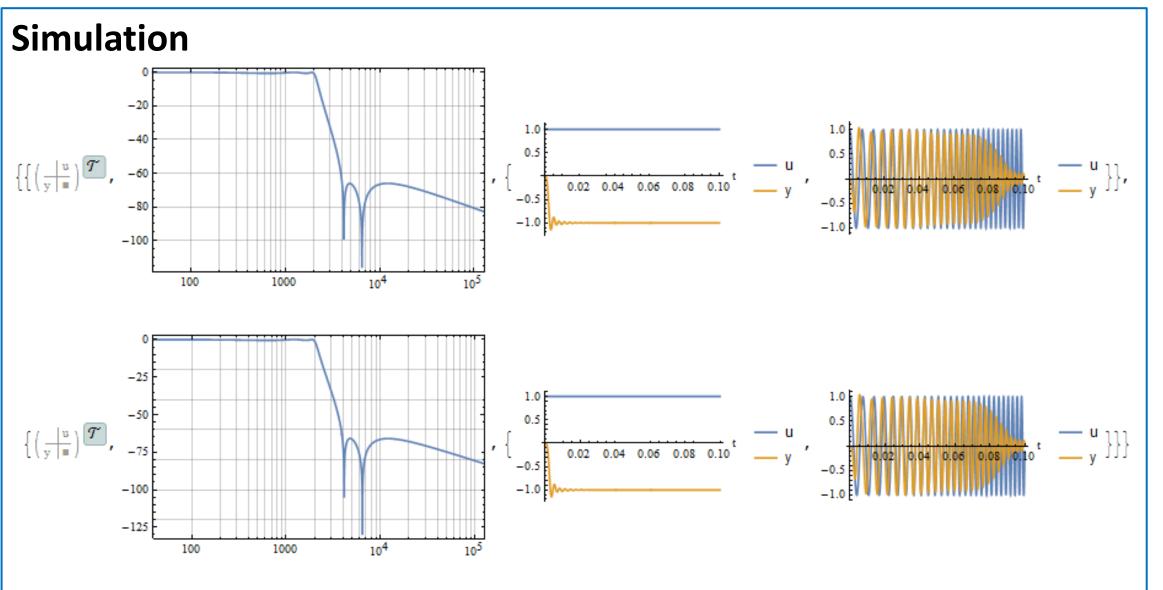
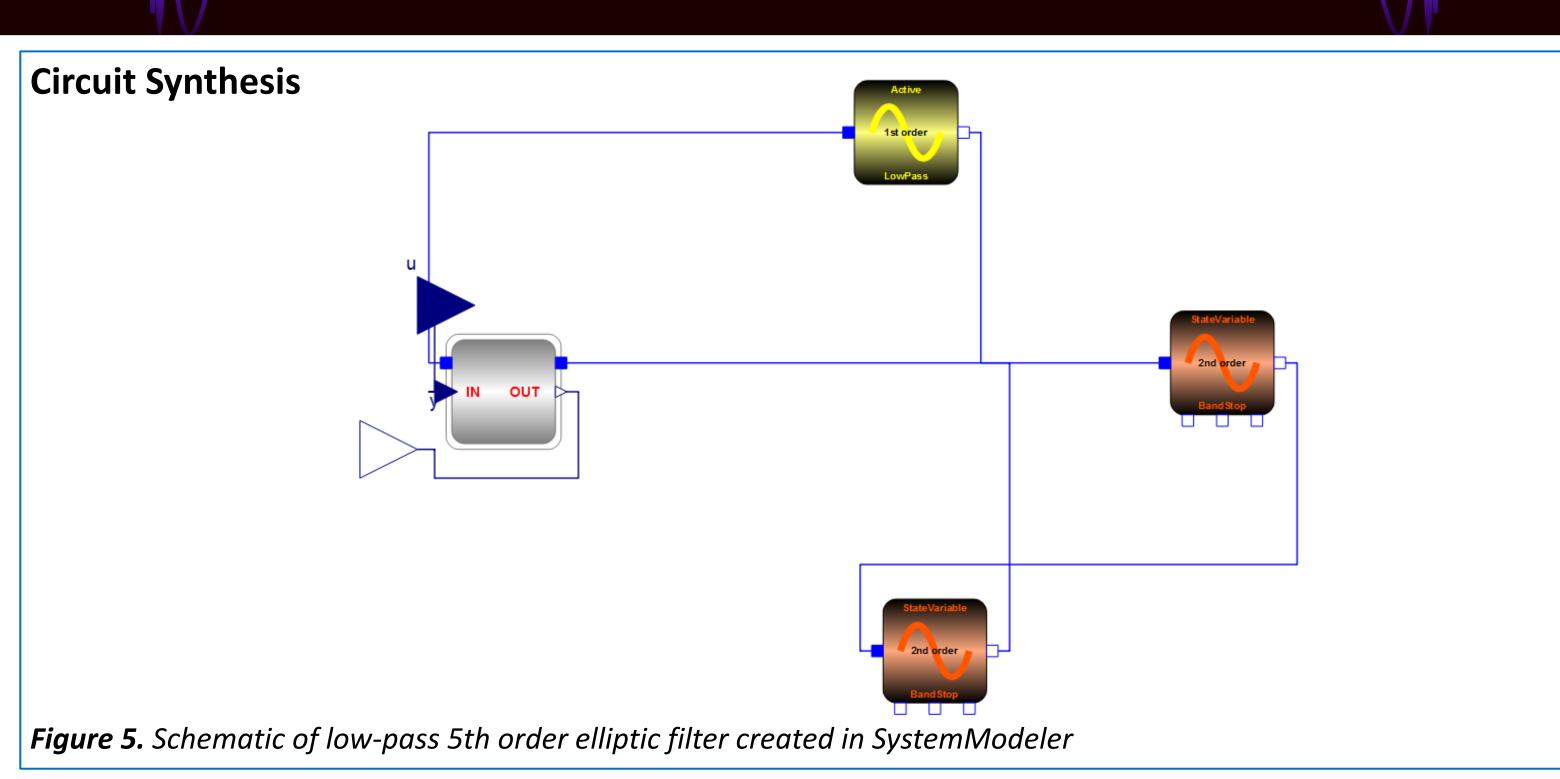
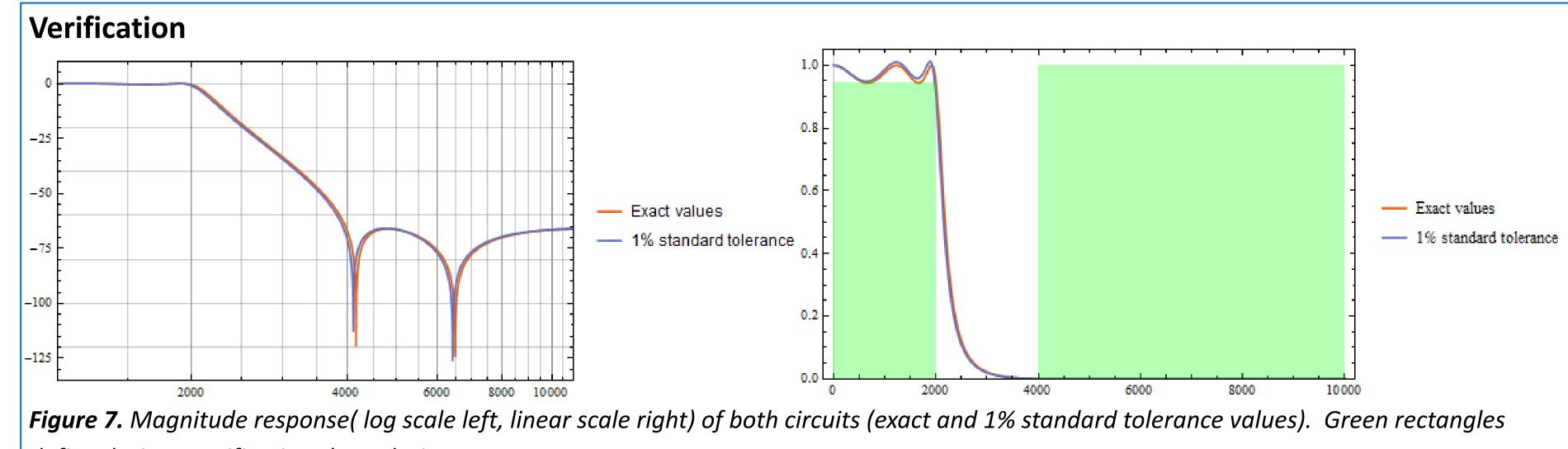


Figure 6. Transfer functions, Bode plot, Step and Chirp response of designed circuit top row - exact component values, bottom row - 1% standard tolerance components).





define design specifications boundaries.